

Morning Tutorials - Monday September 11th, 2017				
Time	AV01.12	AV03.12	AV00.17	AV02.17
	ESSCIRC Tutorials		ESSDERC Tutorials	
	Integrated Power Management in Research and Industry	The Hidden Challenges of 5G	Beyond CMOS	Sensors for the IoT Era
08:00-08:30	Registrations			Registrations
08:30-09:00	Registrations			Introduction and Overview <i>Mirjana Banjevic</i>
09:00-10:00	Integrated Switched-capacitor Power Converters: a Feasible Way to Get High Efficiency Power Conversion in Standard CMOS Technology <i>Gerard Villar Piqué</i>	What is Required of 5G to Make It Attractive for MTC? <i>Yao-Hong Liu</i>	Introduction to Beyond CMOS <i>Iuliana P. Radu</i>	Smart Sensors for Environmental Monitoring <i>Florin Udrea</i>
10:00-10:30	Coffee Break			
10:30-11:30	Integrated Inductive and Hybrid DC-DC Converters <i>Giovanni Frattini</i>	Programmable mm-Wave Transmitters: Generalized PA Architectures for Frequency and Back-off Reconfigurability <i>Kaushik Sengupta</i>	Tunnel FETs – Status and Prospects <i>Nadine Collaert</i>	Sensors for Health Monitoring <i>Patrick Mercier</i>
11:30-12:30	Making Magnetics Disappear - Integrated Magnetics for Power Management <i>Cian O'Mathuna</i>	Implementing Massive MIMO Systems: Coarse Processing Gives Fine Performance <i>Liesbet Van der Perre</i>	Negative Capacitance FETs: Physics, Materials and Devices <i>Asif Islam Khan</i>	Sensors for Automotive Industry <i>Appo van der Wiel</i>
12:30-13:30	Lunch			

Afternoon Tutorials - Monday September 11th, 2017				
Time	AV01.12	AV03.12	AV00.17	AV02.17
	ESSCIRC Tutorials		ESSDERC Tutorials	
	Integrated Power Management in Research and Industry	The Hidden Challenges of 5G	Beyond CMOS	Neuromorphic Computing with Emerging Synaptic Devices
12:30-13:30	Lunch			Lunch
13:30-14:00				Neuro-inspired Computing using Resistive Synaptic Devices: Challenges and Prospects <i>Shimeng Yu</i>
14:00-15:00	Vibrational Harvesters for Wireless Sensing Applications <i>Stefano Stanzione</i>	Integrated Wide-band Baseband Filters and Narrow-band RF Filters for 5G <i>Bram Nauta</i>	Towards Functionality-Enhanced Devices: Controlling the Modes of Operations in Three-Independent-Gate Transistors <i>Pierre-Emmanuel Gaillardon</i>	Analog Synapse Devices based on Interface Resistive Switching for Neuromorphic Systems <i>Hyunsang Hwang</i>
15:00-15:30	Coffee Break			
15:30-16:30	Power Management Design for Highly Integrated Automotive ICs <i>Bernhard Wicht</i>	Challenges in 5G Transmitter Design <i>Christian Fager</i>	Spintronics in the Context of Computation <i>Titash Rakshit</i>	Emerging Memories for Neurocomputing <i>Dmitri Strukov</i>
16:30-17:30	1 kV On-chip, Hard to Resist? <i>Valentijn De Smedt</i>	Opto Implementation Challenges for 5G Networks <i>Francesco Testa</i>	Introduction to Quantum Computing <i>Ronald Hanson</i>	Accelerating Backpropagation Training with Non-Volatile Memory: Devices, Circuits and Architecture <i>Geoffrey Burr</i>

Workshops - Monday September 11, 2017			
AV 04.17	AV 91.12	AP 00.15	AP 01.30
08:00 Registrations	08:00 Registrations	08:00 Registrations	08:00 Registrations
08:30 - 10:10 CONNECT <i>Aida Todri-Sanial</i> <i>Salvatore M. Amoroso</i> <i>Benjamin Uhlig</i> <i>Gage Krieger Hills</i> <i>Esko Kauppinen</i>	08:30 - 10:10 MOS-AK <i>Jim Greer</i> <i>Marcelo Pavanello</i>	09:00 - 10:45 SINANO - NEREID <i>Enrico Sangiorgi</i> <i>Francis Balestra</i> <i>Francisco Ibanez</i> <i>Jouni Ahopelto</i> <i>Clivia Sotomayor Torres</i>	08:30 - 11:15 SEMICONDUCTOR MEMORIES <i>Francisco Gamiz</i> <i>Kilho Lee</i> <i>Manfrest Horstman</i> <i>Elisa Vianello</i>
Coffee 10:10 - 10:40	Coffee 10:10 - 10:30	Coffee 10:45 - 11:00	Coffee 11:15 - 11:45
10:40 - 12:30 CONNECT (Continued) <i>Jean Dijon</i> <i>Benjamin Uhlig</i> <i>Marleen van der Veen</i>	10:30 - 12:30 MOS-AK <i>Wim Schoenmaker</i> <i>Jean-Pierre Raskin</i> <i>Chika Tanaka</i>	11:00 - 12:45 SINANO - NEREID (Continued) <i>Anda Mocuta</i> <i>Yann Deval</i> <i>Montserrat Fernandez-Bolanos</i> <i>Steve Stoffels</i> <i>Stéphane Monfray</i>	11:45 - 13:15 SEMICONDUCTOR MEMORIES (Continued) <i>Pierre Fazan</i> <i>Guohan Hu</i>
Lunch 12:30 - 14:00	Lunch 12:30 - 14:00	Lunch 12:45 - 14:00	Lunch 13:15 - 14:30
14:00 - 15:10 CONNECT (Continued) <i>Shu-Jen Han</i> <i>Bingan Chen</i>	14:00 - 15:00 MOS-AK (Continued) <i>Nicolas Cordero</i> <i>Ashkhen Yesayan</i>	14:00 - 16:00 SINANO - NEREID (Continued) <i>Danilo Demarchi</i> <i>Holger Schmid</i> <i>Georgios Fagas</i> <i>Thanasis Dimoulas</i> <i>Enrico Sangiorgi</i>	14:30 - 16:00 SEMICONDUCTOR MEMORIES (Continued) <i>Philippe Galy</i> <i>Sorin Cristoloveanu</i> <i>Joris Lacord</i>
Coffee 15:10 - 15:40	Coffee 15:00 - 15:30		Coffee 16:00 - 16:30
15:40 - 17:00 CONNECT (Continued) <i>Bernd Gotsmann</i> <i>Aida Todri-Sanial</i>	15:30 - 17:00 MOS-AK (Continued) <i>Denis Flandre</i> <i>Benjamin Iniguez</i> <i>Saravana Maruthamuthu</i>		16:30 - 17:45 SEMICONDUCTOR MEMORIES (Continued) <i>Asen Asenov</i> <i>Andy Pickering</i>

Tuesday September 12th, 2017

Time	AP00.15	AV91.12	AV01.12	AV03.12	AP01.30	AV00.17	AV02.17
08:00-08:30	Vesalius – Registrations						
08:30-09:00	Vesalius – Conference Opening & Welcome (presentation by SSC society and EDS society)						
09:00-09:40	Vesalius – A1L-A: ESSDERC/ESSCIRC Joint Plenary 1: Françoise Chombar, Melexis Engineering a Safe, Clean & Comfortable Future						
09:40-10:20	Vesalius – A2L-A: ESSDERC/ESSCIRC Joint Plenary 2: Peter Real, Analog Devices Navigating without a Moore’s Law Compass						
10:20-11:00	Coffee Break						
11:00-12:20	A3L-B References	A3L-C RF Receivers and Detectors	A3L-D Digital Accelerators	A3L-E Optical Sensors	A3L-F Resistive RAM	A3L-G Cross-Domain Compact Modelling	A3L-H Focus Session: Beyond CMOS Devices I
12:20-14:00	Lunch						
14:00-15:20	A4L-B Amplifiers and Comparators	A4L-C Oscillators	A4L-D Wireline and Optical		A4L-F Alternative Device Simulations	A4L-G Parameter Extraction	A4L-H Focus Session: Beyond CMOS Devices II
15:20-15:50	Coffee Break						
15:50-16:30	Room: AP00.15 A5L-B, ESSCIRC Keynote 1: Yves Baeyens: Solving Capacity Bottlenecks in Fixed Access Comms				Room: AP01.30 A5L-F, ESSDERC Keynote 1: Ian Young: Principles and Trends in Quantum Nano-Electronics & Nano-Magnetics for Beyond-CMOS Computing		
16:40-18:00	A6L-B Energy Harvesting	A6L-C Advanced ADC Techniques	A6L-D Digital-intensive Frequency Synthesis	A6L-E Digital Processors		A6L-G Modelling of Emerging Devices	A6L-H 2D Material Devices
18:00-19:00	ESSCIRC TPC Mtg.				ESSDERC TPC Mtg.		
19:00-21:00	ESSDERC/ESSCIRC 2017 Welcome Reception: Jubilee Hall						

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Wednesday September 13th, 2017

Time	AP00.15	AV91.12	AV01.12	AV03.12	AP01.30	AV00.17	AV02.17
08:30-08:45	Vesalius – Presentation on ESSCIRC/ESSDERC 2018						
08:45-09:25	Vesalius – B1L-A: ESSDERC/ESSCIRC Joint Plenary 3: Hans Stork, ON Semiconductor Smart Power for Automotives						
09:25-10:10	Coffee Break						
10:10-12:10	B2L-B High Speed ADC	B2L-C Transmitters and Power Amplifiers for mm-Wave and IoT	B2L-D Biomedical Circuits and Systems		B2L-F Widebandgap Power Devices	B2L-G Advanced CMOS Characterization and Reliability	B2L-H Emerging Memory Technologies
12:10-13:30	Lunch						
13:30-14:10	Room AP00.15 B3L-B, ESSCIRC Keynote 2: Sven Mattisson, Ericsson - Overview of 5G Requirements & Future Wireless Networks				Room AP01.30 B3L-F, ESSDERC Keynote 2: Siva Sivaram - Storage Class Memories: Desire Meets Reality		
14:20-15:40	B4L-B High Efficiency ADC	B4L-C 5G and mm-Wave Frequency Synthesis	B4L-D Machine Learning and Neuromorphic Computing	B4L-E Linear Regulators	B4L-F FinFet and Nanowire Simulations	B4L-G Traps and Noise	B4L-H 2D Material Integration
15:40-16:00	Coffee Break						
18:00-23:00	Gala Dinner: Autoworld (Brussels)						

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Thursday September 14th, 2017

Time	AP00.15	AV91.12	AV01.12	AV03.12	AP01.30	AV00.17	AV02.17
08:30-09:00	Vesalius -- Awards Ceremony						
09:00-09:40	Vesalius -- C1L-A: Joint Plenary 4: Tetsuo Endoh, Tohoku University Spintronics Applications: STT-MRAM & Non Volatile Logic						
09:40-10:20	Coffee Break						
10:20-12:00	C2L-B Power Management and Harvesting	C2L-C Embedded Memories	C2L-D Wireless SOC	C2L-E Focus Session: Neuromorphic Computing	C2L-F Photonics / Microwave / Harsh Enviroment	C2L-G Advanced CMOS Technology	C2L-H Microfluidics and TFTs
12:00-13:30	Lunch						
13:30-14:10	Room AP00.15 C3L-B, ESSCIRC Keynote 3: Harish Krishnaswamy, Columbia Univ. - Integrated Antenna-Interface Components				Room AP01.30 C3L-F, ESSDERC Keynote 3: David DiVincenzo - Control Systems for Quantum Computers		
14:20-15:40	C4L-B DC-DC Converters	C4L-C Sensor Interfaces	C4L-D Low Power Radios		C4L-F Modelling and Measurement of Alternative Material Devices	C4L-G High Mobility Materials and Nanowires	C4L-H Focus Session: Quantum Computing
19:00	ELECTRONIAD Quiz -- Celebrating Easics 25th Anniversary						
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